



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE

B.Tech I Year II Semester (R19) Supplementary Examinations February 2022

Time: 02.00PM to 05:00PM

| Date/ Day | CE | ME | AGE | EEE | ECE | CSE | CSIT |
|-------------------------|---|--------------------------------------|---|--|---|---|------|
| 11.02.2022 Friday | 19HS0802 Engineering Chemistry (Common to CE,ME & AGE) | | | 19HS0849 Applied Physics | 19HS0851 Semiconductor Physics | 19HS0801 Applied Chemistry (Common to CSE & CSIT) | |
| 14.02.2022 Monday | 19HS0831 Differential Equations and Vector Calculus (Common to CE,ME,AGE,EEE & ECE) | | | | | 19HS0835 Probability & Statistics (Common to CSE & CSIT) | |
| 16.02.2022 Wednesday | 19CE0102 Strength of Materials - I | 19ME0303 Materials Engineering | 19EE0240 Basic Electrical and Electronics Engineering (Common to AGE,CSE & CSIT) | 19EE0201 Electrical Circuits - I | 19EE0239 Basic Electrical Engineering | 19EE0240 Basic Electrical and Electronics Engineering (Common to AGE,CSE & CSIT) | |
| 18.02.2022 Friday | 19CS0501 Python Programming (Common to CE, EEE, ME & AGE) | | | | 19HS0810 Communicative English (Common to ECE,CSE & CSIT) | | |
| 21.02.2022 Monday | 19ME0302 Engineering Graphics (Common to CE,EEE,ME & AGE) | | | | 19EC0401 Switching Theory and Logic Design | 19CS0503 Digital Logic Design (Common to CSE & CSIT) | |

Note: 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.

CONTROLLER OF EXAMINATIONS

Controller of Examinations
Siddharth Institute of Engineering & Technology
(AUTONOMOUS)
PUTTUR - 517 583, Chittoor (DIST.) A.P.

PRINCIPAL

PRINCIPAL
Siddharth Institute of Engineering & Technology
Siddharth Nagar
PUTTUR - 517 583, Chittoor (Dist.)